

Optimization of Baud-Rate Timing Recovery for Equalization

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Abstract—This paper suggests the use of a prefilter before baud-rate timing recovery [1]. It is shown that the timing phase of the baud-rate timing synchronizer can be set to a desirable timing phase, at which point a digital equalizer performs best, by proper prefiltering. The procedure for the optimal design of such a prefilter is developed under the assumption that the channel impulse response and desirable timing phase for equalization are known. Application of the proposed scheme to the gigabit Ethernet system demonstrated that prefiltering can improve receiver performance.

Index Terms—Baud-rate timing recovery, equalization, gigabit Ethernet, prefilter.

I. INTRODUCTION

ALTHOUGH the performance of digital equalization, which compensates for the channel after sampling the received signal, is influenced by a sampling timing phase [2], such equalization has been mostly ignored in developing the baud-rate timing recovery scheme in [1]. Accordingly, to improve receiver performance, it is desirable to employ a baud-rate timing recovery method whose timing phase is set to an optimal phase, at which point the digital equalizer performs best and the timing jitter is minimized. The goal of this paper is therefore to develop such a timing synchronization technique.

The proposed method suggests the use of a prefilter before timing recovery.¹ In particular, it will be shown that the baud-rate timing recovery in [1] has the desirable properties for equalization, if a proper prefilter is employed. A procedure for the optimal design of a prefilter is developed under the assumption that the channel impulse response is known. Application of the proposed scheme to the gigabit Ethernet system (1000BASE-T) [3] demonstrated that prefiltering can improve receiver performance.

II. SYSTEM DESCRIPTION

A. Description of Clock Synchronizer

The baseband system model considered in this paper is shown in Fig. 1 (for the time being, the prefilter is ignored). The received signal which is sampled at $t = kT + \tau_k$ is expressed as

$$z_k = \sum_{i=-\infty}^{\infty} a_{k-i}g(iT + \tau_k) + n(kT + \tau_k) \quad (1)$$

Paper approved by L. Vandendorpe, the Editor for Transmission Systems of the IEEE Communications Society. Manuscript received April 15, 2000; revised October 15, 2000, and July 15, 2001.

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Publisher Item Identifier S 0090-6778(02)03510-9.

¹Prefilters that can reduce the timing jitter caused by a data pattern have been introduced in [4] and [5].

where $\{a_k\}$ is the transmitted symbol, τ_k is the sampling timing phase, $g(t)$ is the impulse response describing the cascade of the transmission filter, channel, and receiver filter, and $n(t)$ is additive white noise with the variance $\sigma_n^2 = N_o/2$. One of the timing error detectors (TEDs) in [1], which has been recommended for practical applications and which will be considered hereafter, is represented as

$$e_k = \frac{1}{2E[a_k^2]} (\hat{a}_k z_{k-1} - \hat{a}_{k-1} z_k) \quad (2)$$

where e_k is the TED output and \hat{a}_k is an estimate of a_k . Let $f(\tau) = E[e_k | \tau_k = \tau]$, then $f(\tau) = (1/2)[g(-T + \tau) - g(T + \tau)]$. $f(\tau)$ is referred to as the timing function. The sampling phase τ_k is updated according to the following recursive equation:

$$\tau_{k+1} = \tau_k - \gamma e_k \quad (3)$$

where γ is a constant. When $g(t)$ is a raised-cosine pulse, the timing function $f(\tau)$ monotonically increases for $-0.5T \leq \tau \leq 0.5T$ and it has only one root at $\tau = 0$. In this case, τ_k in (3) tends to converge to zero for proper values of γ . The root of $f(\tau)$ is referred to as the stable equilibrium point and is denoted by τ_o .

B. Prefiltering Received Signal

Suppose that a prefilter with the impulse response $\{w_i; -L \leq i \leq L\}$ is employed (Fig. 1). The prefilter output can be expressed as $\tilde{z}_k = \sum_{i=-L}^L w_i \cdot z_{k-i}$ and the TED output e_k can be obtained by replacing $\{z_k\}$ in (2) with $\{\tilde{z}_k\}$. Prefiltering reshapes the received signal. The received pulse shape $g(t)$ is thus modified as $\tilde{g}(t) = \sum_{i=-L}^L w_i g_i(t)$ where $g_i(t) = g(t - iT)$. The timing function is rewritten as $\tilde{f}(\tau) = (1/2)[\tilde{g}(-T + \tau) - \tilde{g}(T + \tau)]$. Using vector notations, $\tilde{f}(\tau) = (1/2)\mathbf{w}^T [\mathbf{g}_{-1}(\tau) - \mathbf{g}_1(\tau)]$ where $\mathbf{w} = [w_{-L}, \dots, w_L]^T$ and $\mathbf{g}_i(\tau) = [g_{i+L}(\tau), \dots, g_{i-L}(\tau)]^T$. It is assumed hereafter that $\tilde{f}(\tau)$ is monotonically increasing in $-0.5T \leq \tau \leq 0.5T$ and has only one root at $\tau = \tau_s$. Then τ_k in (3) approaches τ_s , which is the stable equilibrium point of $\tilde{f}(\tau)$. The purpose of prefiltering is to shape the received signal so as to minimize the variance of the timing jitter at $\tau = \tau_s$. Next, the steady-state jitter variance of the baud-rate clock synchronizer with prefiltering is derived following the procedure in [5]. The loop equation in (3) is rephrased as $\tau'_{k+1} = \tau'_k - \gamma [\tilde{f}(\tau_k) + N_k]$ where $\tau'_k = \tau_k - \tau_s$, and $N_k = e_k - \tilde{f}(\tau_k)$ is the loop noise. If $\tilde{f}(\tau_k)$ is approximated by $A_s \tau'_k$ in the vicinity of the stable equilibrium point $\tau = \tau_s$ where $A_s = [d\tilde{f}(\tau)/d\tau]_{\tau=\tau_s}$, then the loop equation can be rewritten as

$$\tau'_{k+1} = (1 - \gamma A_s) \tau'_k - \gamma N_k. \quad (4)$$

The steady-state jitter variance $\sigma_J^2 = \lim_{k \rightarrow \infty} E[\tau_k'^2]$ can be found from (4) following standard linear analysis techniques:

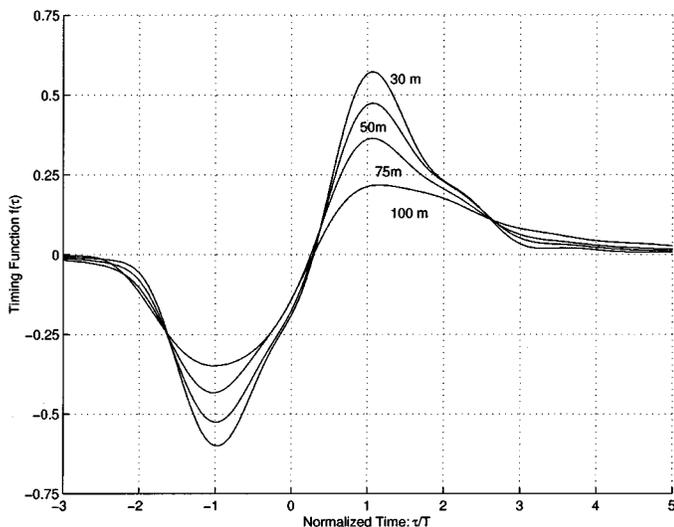


Fig. 2. Timing functions $\{f(\tau)\}$ for different channel length.

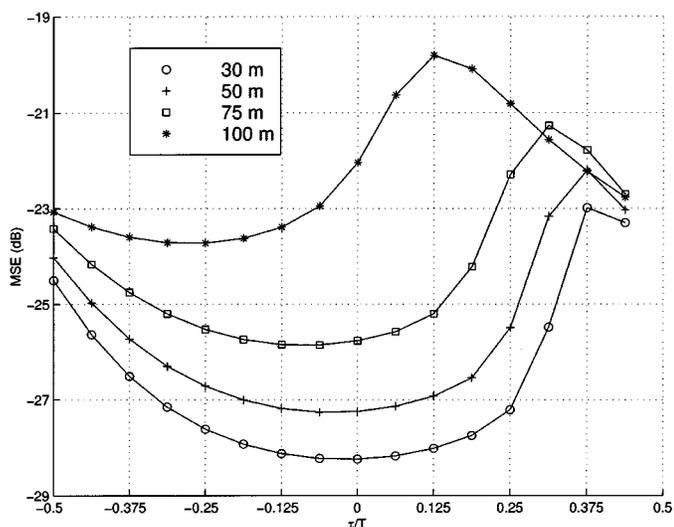


Fig. 3. Numerically calculated MSEs of the DFE.

results are shown in Fig. 3. It can be seen that the influence of the sampling phase on the MSE is significant. For example, for the 30-m channel the difference between the maximum and minimum MSE values was larger than 5 dB. As expected, the MSEs increased as the channel distance increased. The optimal timing phase τ_{opt} varied depending on the channel distance. For example, $\tau_{\text{opt}} = 0$ for the 30-m channel and $\tau_{\text{opt}} = -0.125T$ for the 75-m channel. Consequently, it is impossible to design an optimal prefilter that can improve the receiver performance for all channels. Accordingly, in what follows, a prefilter was designed that optimized the timing phase of the 100-m channel, which was the worst. For the other channels, this optimization will produce a nonoptimal timing phase; however, in most practical cases, after prefiltering the MSEs should still be smaller than those associated with the 100-m channel without prefiltering. This is demonstrated through the following simulation.

Suppose that $E_s/N_o = 30$ dB, $B_L T = 0.01$, and $L = 2$ (5-tap prefilter). For the 100-m channel, $\tau_{\text{opt}} = -5/16T$ and $A_o = 0.5$. From (10), the optimal weight was evaluated as $\mathbf{w}_o =$

TABLE I
STABLE EQUILIBRIUM POINTS, JITTER VARIANCE σ_J^2 AND MSE
OF DFE WHEN $E_s/N_o = 30$ dB, $B_L T = 0.01$ AND $L = 2$

| channel | No Prefilter | | | 5-tap Prefilter | | |
|---------|--------------|-----------------------|----------|-----------------|-----------------------|----------|
| | τ_o | σ_J^2 | MSE | τ_s | σ_J^2 | MSE |
| 30 m | 0.262T | 2.14×10^{-4} | -22.1 dB | -0.383T | 2.44×10^{-4} | -20.3 dB |
| 50 m | 0.251T | 3.41×10^{-4} | -20.1 dB | -0.370T | 2.75×10^{-4} | -20.1 dB |
| 75 m | 0.226T | 6.12×10^{-4} | -18.0 dB | -0.369T | 2.82×10^{-4} | -20.3 dB |
| 100 m | 0.259T | 2.94×10^{-3} | -14.5 dB | -0.312T | 1.27×10^{-4} | -20.8 dB |

$[-0.1368 \ 0.0864 \ 1.1827 \ -0.9947 \ 0.1752]^T$. The steady-state jitter variance and MSE of the DFE were empirically estimated through computer simulation, assuming that echo and cross-talk signals were absent. The results are listed in Table I. It can be seen that a considerable reduction in σ_J^2 and MSE were achieved for the 100-m channel with the optimal prefilter: σ_J^2 after prefiltering was about 1/20 of the original and the MSE gain was about 6 dB. Some performance improvement was also achieved for the 75-m channel.³ However, the prefiltering somewhat increased the σ_J^2 and MSE in the case of the 30-m channels. The largest MSE after prefiltering was -20.1 dB (50 m channel), which was 5.6 dB smaller than the largest MSE before prefiltering (-14.5 dB, 100 m channel); therefore, the prefilter decreased the largest MSE by 5.6 dB.⁴

V. CONCLUSION

The design of a prefilter for baud-rate timing recovery was investigated. An optimization problem that minimizes the steady-state jitter variance under a constraint regarding the location of the steady-state timing phase was formulated, and a closed-form expression of the optimal filter coefficients was derived using Lagrange multipliers. The prefilter was applied to the 1000BASE-T Ethernet system. Accordingly, it was shown through simulation that prefiltering before the timing recovery can reduce the MSE of the equalizer.

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³This fact indicates the robustness of prefiltering to channel variations: the prefilter performance is not sensitive to small variations of the channel and the optimal timing phase for which the prefilter is optimized. When channel variations are large, one may design multiple prefilters and select one of them.

⁴In some applications, use of a single prefilter may not significantly reduce the MSE. For those cases, employing multiple prefilters and selecting one of them, which causes the smallest MSE, may be considered.